Grade 10 | Logic Gates with Boolean Function | MCO Paper

- 1. Consider a logic circuit with three inputs P, Q, R and one output F. The following is observed in this circuit: (2015)
 - If any single input is 1, then output F is 0, irrespective of what the remaining inputs are.
 - If all inputs are 0, then the output F is 1.

Which of the following is the most likely logic gate configuration for the circuit?



- To qualify in an examination, candidates have to pass one compulsory subject S₁ and one of the three optional subjects S₂, S₃ and S₄. Which of the following Boolean expressions correctly represents this scenario? (2015)
 - 1) $S_1 AND (S_2 AND S_3 AND S_4)$
 - 2) $S_1 AND (S_2 OR S_3 OR S_4)$

3.

- 3) S₁ OR (S₂ AND S₃ AND S₄)
 4) S₁ OR (S₂ OR S₃ OR S₄)
- Consider the following logic circuit diagram.



Which of the following is equivalent to the above logic circuit?



4. Which of the following logic circuits represents the given truth table?

(2016)

(2016)



5. Which of the following truth tables is equivalent to the logic circuit given below? (2017)



Α	B	Output	A	B	Output	A	B	Output	Α	B	Output
0	0	1	0	0	0	0	0	0	0	0	0
0	1	1	0	1	0	0	1	1	0	1	1
1	0	1	1	0	1	1	0	0	1	0	1
1	1	0	1	1	0	1	1	0	1	1	0
(1)			(2)			(3)			(4)		

6. Which of the following logic circuits has a truth table equivalent to the truth table of the logic circuit shown on the right hand side? (2017)



- 1) I and II only
- 2) I and III only
- 3) II and III only
- 4) All I, II and III

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- 9. If 0 and 1 respectively are given as inputs for X in the following logic circuit, what would be the two respective outputs at Y? (2019)
 - 1) A, B
 - 2) A, B
 - 3) B, Ā
 - 4) B, A
- 10. Which of the following Boolean expression is equivalent to the output of the given logic circuit?

X

R

- 1) $(X . Y) + (\overline{Y} + X)$
- 2) $(X + Y) . (\overline{Y} . X)$
- 3) $(X + Y) . (X . \overline{Y})$
- 4) $(X \cdot Y) + (Y + \overline{X})$
- 11. Consider the following logic circuit:



(2020)

(2020)



Which of the following logic circuits has a truth table equivalent to the above logic circuit?

